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28

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
In re the Application of

Akihiko EBINA et al.

Attn: OIPE

Application No.: 10/052,255

Docket No.: 111778

Filed: January 23, 2002

FILE COPY

For: METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE INCLUDING NONVOLATILE SEMICONDUCTOR MEMORY  
DEVICES

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TECHNOLOGY CENTER 2800

REQUEST FOR CORRECTION OF PALM RECORDS

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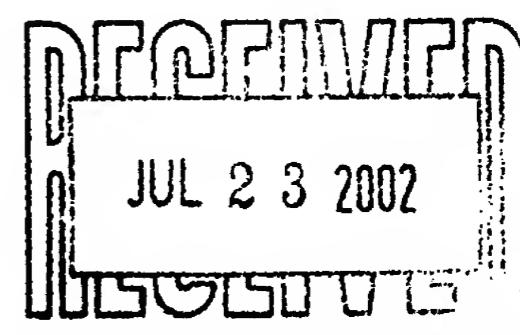
Sir:

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Respectfully submitted,

James A. Oliff  
Registration No. 27,075

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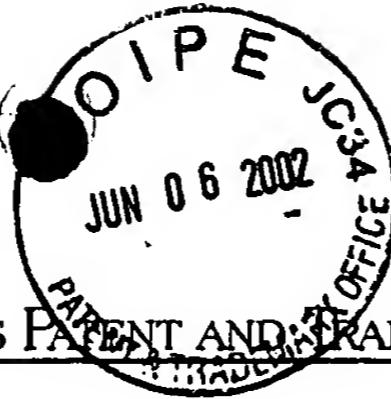
Date: June 6, 2002

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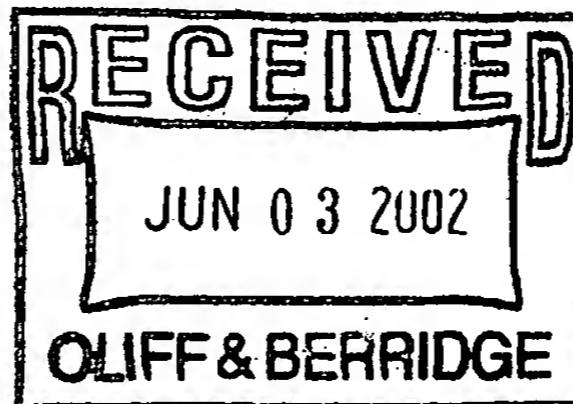
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/052,255	01/23/2002	2812	870	111778	11	15	1

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CONFIRMATION NO. 1930

UPDATED FILING RECEIPT



\*OC000000008212686\*

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**Applicant(s)**

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**Domestic Priority data as claimed by applicant****Foreign Applications**

JAPAN 2001-021931(P) 01/30/2001

**If Required, Foreign Filing License Granted 02/19/2002****Projected Publication Date: 09/12/2002****Non-Publication Request: No****Early Publication Request: No****Title**

Method of manufacturing semiconductor integrated circuit device including nonvolatile

semiconductor memory devices

Preliminary Class

438

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